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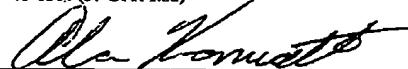
Client Docket No. CFP00353

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re App : Herng-Jer Lee et al. : Examiner James C. Kerveros
Serial No. : 10/827,507 : Art Unit 2138
Filed : April 19, 2004 : Confirmation No. 3480
For : Method of Scan Chain Reordering for Lowering VLSI Power Consumption

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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ALEXANDRIA, VA 22313-1450, ON OCTOBER 26, 2006 TO FAX
NO.: 571-273-8300 (37 CFR 1.8a)

SUPPLEMENTAL AMENDMENT

Dear Sir:

In continuation of the AMENDMENT mailed on September 18, 2006, please amend the above application as follows: